## DECLARATION FOR NON-PROVISIONAL PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below at 201 et seq. beneath my name.

I believe I am the original, first and sole inventor if only one name is listed at 201 below, or an original, first and joint inventor if plural names are listed at 201 et seq. below, of the subject matter which is claimed and for which a patent is sought on the invention entitled

## INSTALLATION FOR TREATING SEMICONDUCTOR WAFERS

	docket No. <b>4717-11100</b> as Applicati	on No.	·			
I hereby state that I have reviewed amendment referred to above.	and understand the contents of the al	bove identified application, i	including the claims,	as amended by any		
I acknowledge the duty to disclose § 1.56.	information known to me to be materi	al to patentability as defined	in Title 37, Code of I	Federal Regulations,		
	nefits under Title 35, United States Co so identified below any foreign applica s claimed:					
EARLIEST FOREIGN APPLICATION(S), IF ANY, FILED PRIOR TO THE FILING DATE OF THE APPLICATION						
APPLICATION NUMBER	COUNTRY	DATE OF FILING (day, month, year)	PRIOR	PRIORITY CLAIMED		
0308392	France	July 9, 2003	☐ YES	□ NO		
			☐ YES	□ NO		
I hereby claim the benefit under Ti	tle 35, United States Code, § 119(e) o	of any United States provision	nal application(s) lis	ted below.		
PROVISIONAL APPL	FILING DATE					
matter of each of the claims of this of Title 35, United States Code § 1	tle 35, United States Code, § 120 of ar application is not disclosed in the prior 12, I acknowledge the duty to disclose ons, § 1.56 which became available be dication:	United States application in information known to me whi	the manner provided ich is material to pater	by the first paragraph ntability as defined in		
NON-PROVISIONAL FILING DATE		STATUS  PATENTED PENDING ARANDONED				
APPLICATION NO.	1	PATENTED	PENDING	ABANDONED		

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SEN	D CORRESPONDE	NCE TO: WINSTON & STRAV PTO Customer 1		DIRECT TELL Allan A. Fanucci at 212	EPHONE CALLS TO 2-294-3311	
2	FULL NAME OF INVENTOR	POULENARD	FIRST NAME  Michel	MIDDLE NAME		
0	RESIDENCE & CITIZENSHIP	Voreppe	STATE OR FOREIGN COUNTRY  France	France	COUNTRY OF CITIZENSHIP  France	
	POST OFFICE ADDRESS	152 rue de l'Hoirie	Voreppe	STATE OR COUNTRY France	38340	
		SIGNATURE OF INVENTOR 201		Sept.	ofl	

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Michel POULENARD

Application No.:

Group Art Unit:

Filing Date:

Examiner:

For:

INSTALLATION FOR TREATING SEMICONDUCTOR WAFERS

Attorney Docket No.: 4717-11100

## POWER OF ATTORNEY BY ASSIGNEE AND EXCLUSION OF INVENTOR(S) UNDER 37 C.F.R. 3.71

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

The undersigned assignee of the entire interest in the above-identified subject application hereby appoints Allan A. Fanucci (Reg. No. 30,256), Daniel J. Hulseberg (Reg. No. 36,554) and Stephan J. Filipek (Reg. No. 33,384) of WINSTON & STRAWN LLP (Customer No. 28765) to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith.

Please direct all correspondence for this application to Customer No. 28765 to the attention of Allan A. Fanucci (telephone 212-294-3311, facsimile 212-294-4700).

An assignment of the entire interest in the above-identified subject application is submitted herewith for recording and a copy is attached. The undersigned has reviewed this assignment and, to the best of his knowledge, title is in the assignee seeking to take action in this application and that he is empowered to act on its behalf.

ASSIGNEE:	S.O.I. TEC SILICON ON INSULATOR TECHNOLOGIES S.A.		
Signature:	ffeles		
Date of Signature:	026963		
Typed Name:	E. HUYGHE		
Position/Title:	IP MAURGER		
Address:	Pour Technologique des Fontourico		
	38926 Bernin, FRANCE		
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